

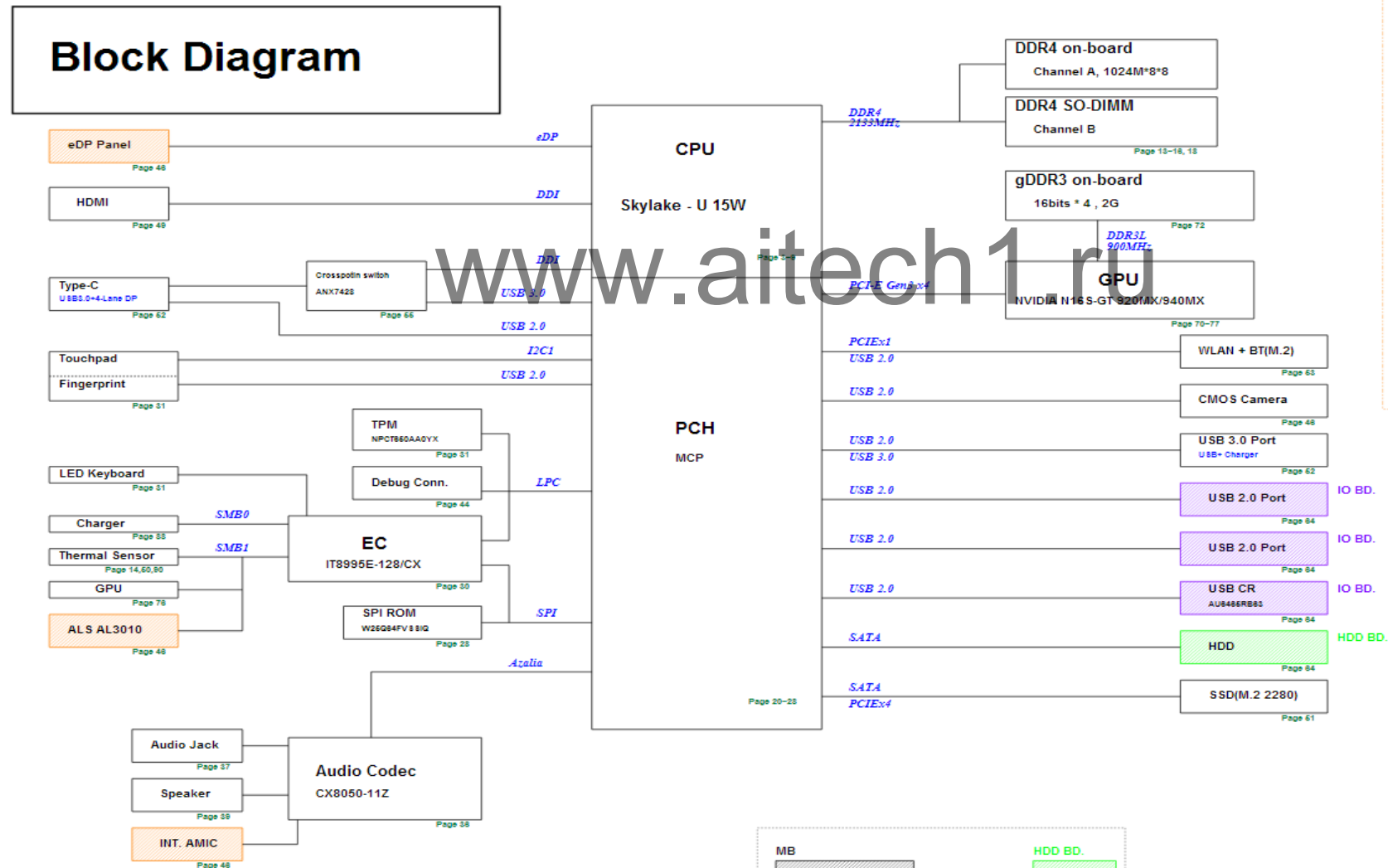
BLOCK DIAGRAM

UX310UA/UV/UQ SCHEMATIC Revision R1.2

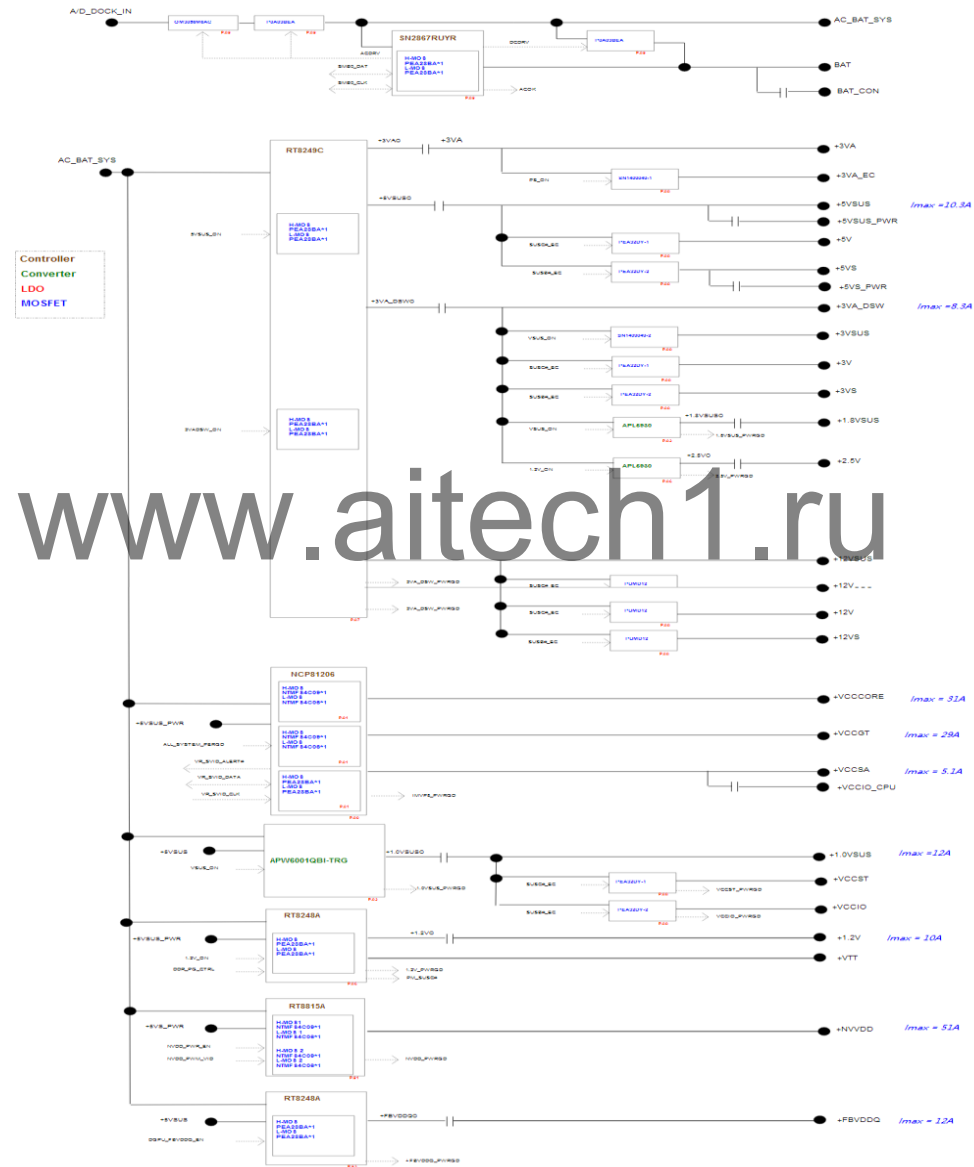
UA : INTEL HD GRAPHIC

UV: NVIDIA GeForce 920MX(C.S N16V-GMR1-S-A2 FCBGA595), GDDR3 2G (256 x 16 x 4)

UQ: NVIDIA GeForce 940MX(C.S N16S-GTR-S-A2 FCBGA595), GDDR3 2G (256 x 16 x 4)



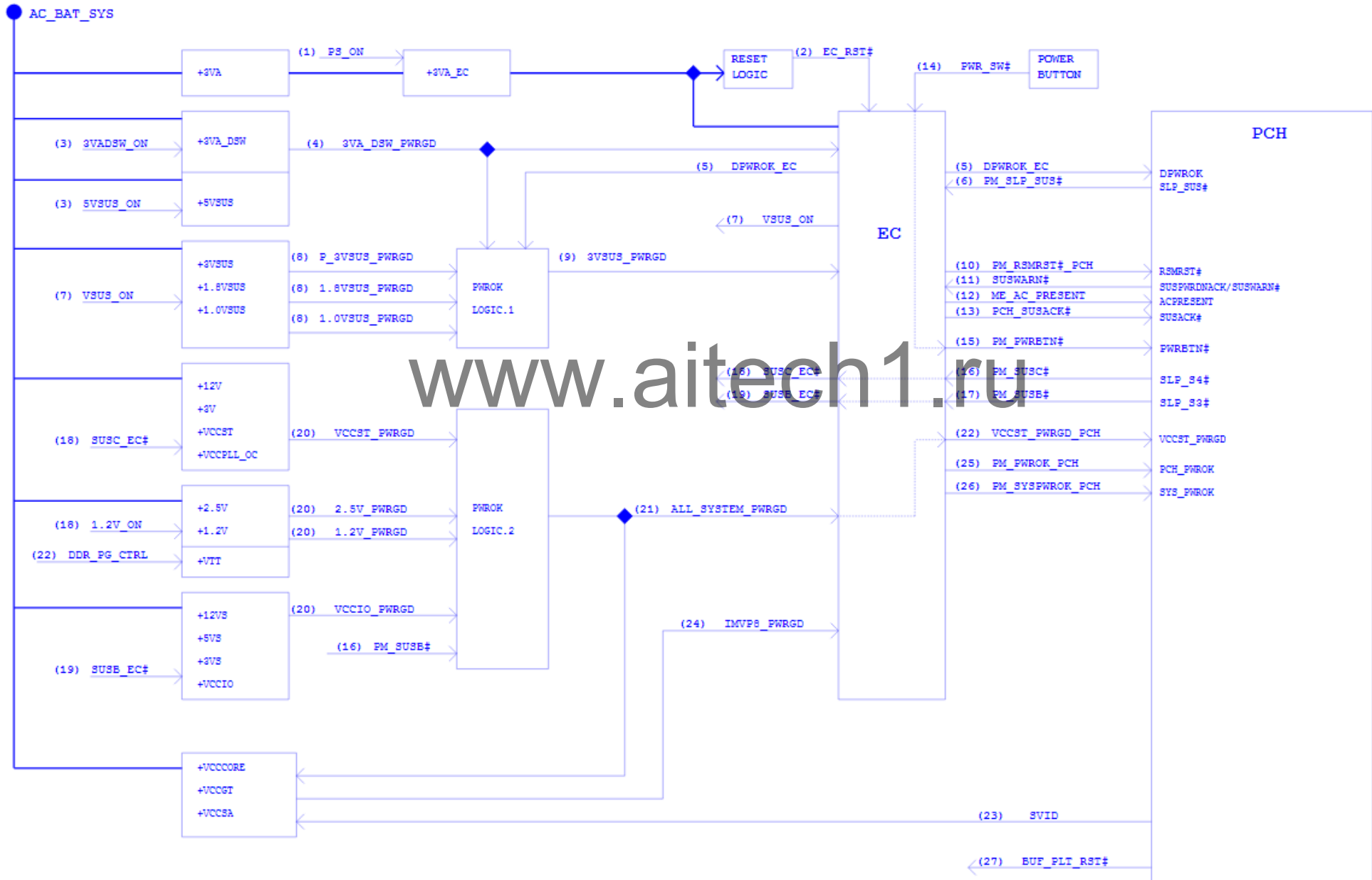
POWER FLOW



POWER ON SEQUENCE

UX310 Power On Sequence Diagram Rev.0.1

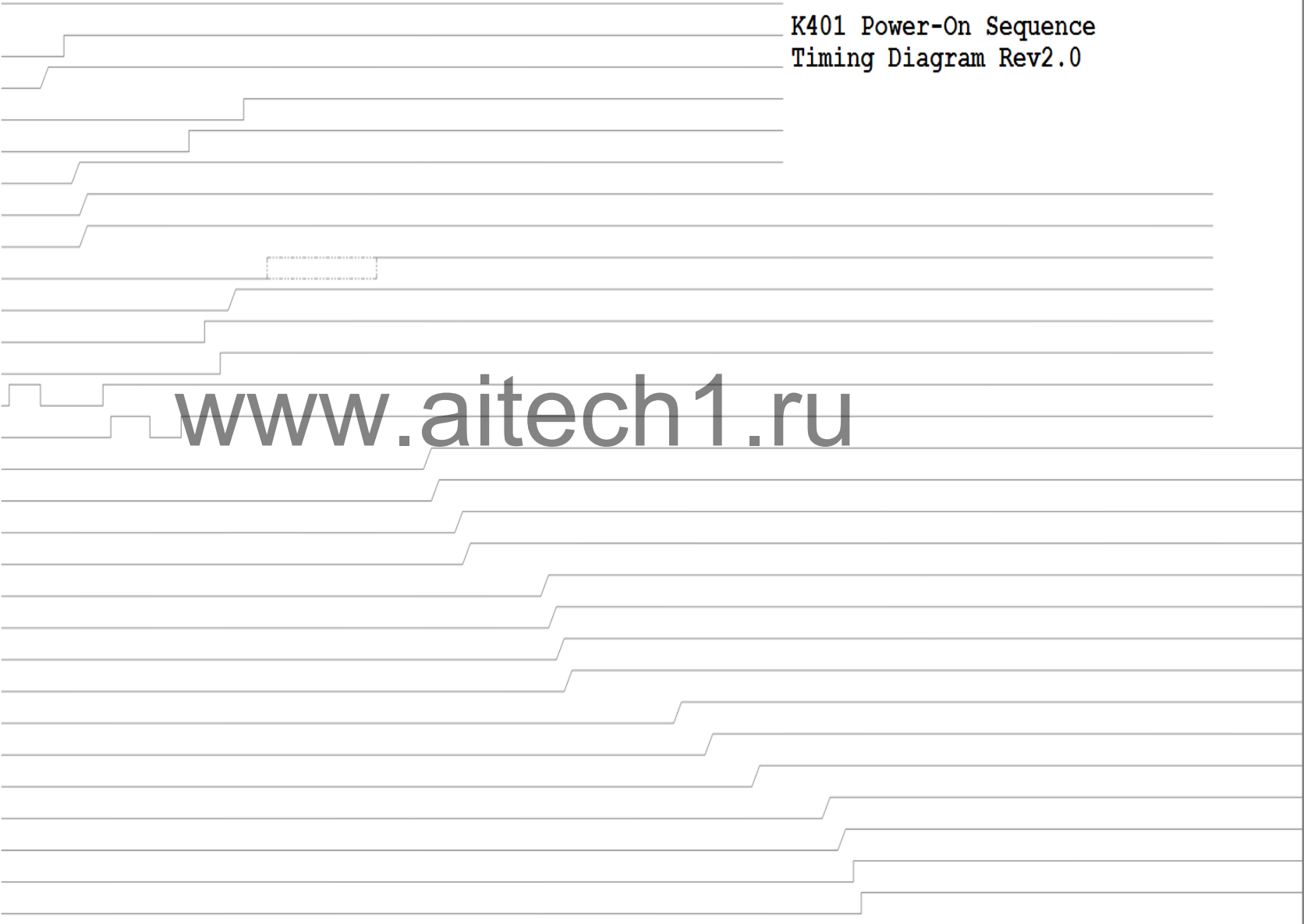
AC-IN Mode



AC POWER ON SEQUENCE

AC-IN Mode

- 2 3VA_EC
- 3 EC_RST#
- +3VA_DSW
- 11 PM_SLPSUS#
- 7 DFWROK_R
- 4 3VSUS_ON
- +3VSUS/+5VSUS
- 5 +3VSUS_FWRGD
- 12 ME_SusPwrDnAck_R
- 10 PM_RSMRST#_PCH
- 8 ME_AC_PRESENT_PCH
- 9 SUSACK_R#
- 1 PWR_SW#
- 6 PM_PWRBTN#
- 13 PM_SUSC#
- 14 PM_SUSB#
- 15(a) PM_SLP_A#
- 15(b) SLP_LAN#
- 16 SUSC_EC#
- +12V
- 17 SUSB_EC#
- +12VS
- 18 ALL_SYSTEM_FWRGD
- 19 PM_PWROK_PCH
- 21 H_CPUFWRGD
- +VCORE
- 20 CORE_FWRGD
- 22 PM_SYSPWROK_PCH
- 23 BUF_PLT_RST#



DC POWER ON SEQUENCE

DC-IN Mode

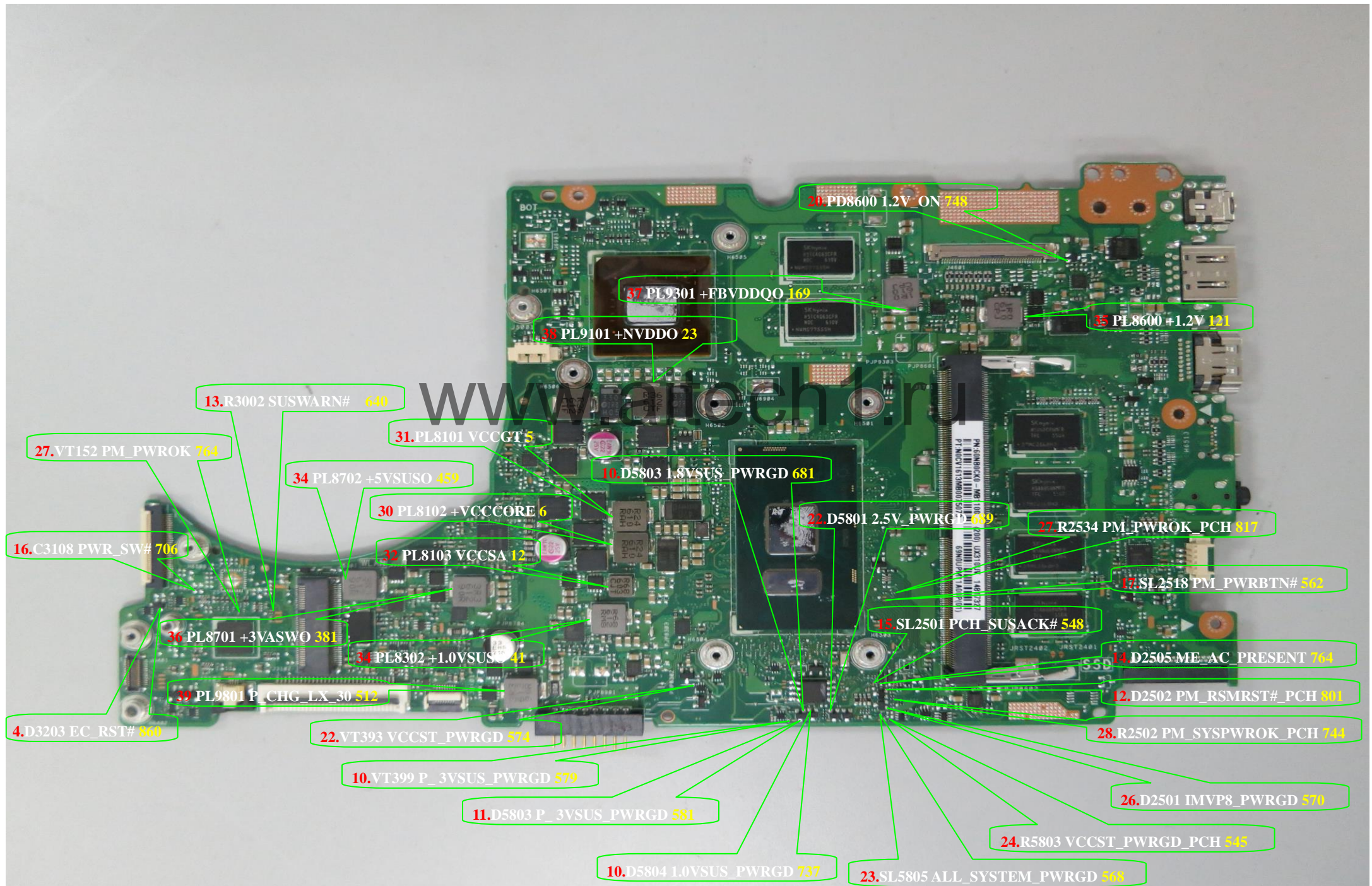
1 +3VA/+5VA/+3VA_EC
2 EC_RST#
VccDSW
3 PM_SLP_SUS#
4 PM_DFWROK
5 FWR_SW#
6 VSUS_ON
+3VSUS/+5VSUS
7 SUS_PWRGD
8 ME_SusPwrDnAck
9 PM_RSMRST#
10 ME_AC_PRESENT
11 PM_SUSACK#
12 PM_FWRBIN#
13(a) PM_ME_SLP_LAN#
13(b) PM_ME_SLP_A#
14 PM_SUSC#
15 PM_SUSB#
+105VM_LAN
+1.05VM/+3VM
16 SUSC_EC#
+1.5V/+3V/+5V
17 USB_EC#
+0.75VS/+1.5VS//+1.6VS/+3VS/+5VS
18 ME_+VM_PWRGD
19 ME_PWROK
20 SYSTEM_PWRGD
+VTT_CPU
21 +VTT_CPU_PWRGD
+0.8VS
22 +0.8VS_PWRGD
23 ALL_SYSTEM_PWRGD
24 PM_FCHFWROK
25 H_DRAM_PWRGD
26 H_CPUPWRGD
27 SVID
+VCORE
+VccGFX
28 VRM_PWRGD
29 PM_SYSPWROK
30 BUF_PLT_RST#

U53 Power-On Sequence
Timing Diagram Rev.0.14



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Signal Measure Point-Bottom



Signal Measure Point-Top

